



## ***NEWS RELEASE***

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### **Bluespec Joins Xilinx ESL Initiative**

*Will Work Cooperatively to Advance FPGA Designer Productivity with ESL Synthesis*

**Waltham, Mass.** — March 13, 2006 — Bluespec Inc., developer of the only ESL synthesis toolset for control logic and complex datapaths in chip design, has joined the Xilinx, Inc. ESL for FPGA Initiative.

Xilinx and Bluespec will work cooperatively to advance field programmable gate array (FPGA) designer productivity through the use of its electronic system level (ESL) synthesis software. The companies will collaborate to improve the value of ESL software with better quality of results, integration of their existing environments and tool interoperability for implementing designs on Xilinx FPGAs.

The Xilinx ESL Initiative was created to expand awareness for this new high-level design methodology within the FPGA community and to promote the benefits of utilizing ESL tools to target programmable hardware implementations.

“We are most pleased that Bluespec shares our vision for expanding the reach of ESL to new FPGA users and applications,” remarks Steve Lass, director of Software Product Marketing at Xilinx. “Their focus on enabling timely, quality of results for ESL synthesis to gates is an important component of the overall ESL methodology. Their participation in the ESL Initiative ensures that hardware designers will have access to world class productivity enhancing solutions for implementing designs in programmable logic.”

Concludes George Harper, vice president of Bluespec: “Xilinx has a well-earned reputation for innovation and forward thinking, and we are committed to collaborating with Xilinx to promote the power of ESL methodologies and ESL synthesis.”

For more details, contact Bluespec’s George Harper at (781) 250-2200 or via email at [george.harper@bluespec.com](mailto:george.harper@bluespec.com). Or, visit the Bluespec website located at: <http://www.bluespec.com>.

### **About Xilinx ESL Initiative**

The Xilinx ESL Initiative is a multi-faceted program aimed at proliferating high-level design methodologies and tool flows for FPGAs. The goal is to make it easier for hardware designers and software programmers to leverage Xilinx programmable devices for their next generation systems. Xilinx and participating companies are focused on improving ease of use, quality of results and interoperability standards through technical collaboration, cooperative marketing and joint educational activities. For more information about the Xilinx ESL Initiative, visit [www.xilinx.com/esl](http://www.xilinx.com/esl).

## **About Bluespec**

Bluespec Inc. manufactures an industry standards-based Electronic Design Automation (EDA) toolset that significantly raises the level of abstraction for hardware design while retaining the ability to automatically synthesize high-quality RTL, without compromising speed, power or area. The toolset, the only one focused on control and complex datapaths, allows ASIC and FPGA designers to reduce design time, bugs and re-spins that contribute to product delays and escalating costs. More information can be found on [www.bluespec.com](http://www.bluespec.com) or by calling (781) 250-2200.

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