

NEWS RELEASE

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Bluespec Wins 2006 DesignVision Finalist Award

Bluespec ESL Synthesis Chosen as Finalist in the ASIC, IC Design Tools Category

Waltham, Mass. — January 23, 2006 — The International Engineering Consortium and the 2006 DesignCon Team today informed Bluespec Inc. that the Bluespec Compiler and Simulator toolset, the only electronic system level (ESL) synthesis solution for control logic and complex datapaths in chip design, has been chosen as a finalist in the ASIC and IC Design Tools category of this year's DesignVision Awards. Winners of the DesignVision Awards will be announced Tuesday, February 7, during DesignCon, which will be held February 6-9 at Santa Clara Convention Center in Santa Clara, Calif.

The DesignVision Awards program recognizes technologies, applications, products, and services judged to be the most unique and beneficial to the industry. The DesignVision Awards also honor corporations and individuals for innovative contributions and developments that have proven important to society.

Dr. Barry Sullivan, DesignCon 2006 program director, commented Friday on the award program: "DesignVision Awards recognize innovative products and services that support the work of electronic design engineers, the core audience at DesignCon. The

IEC is pleased to provide this recognition to companies whose products exemplify our standard of service to the industry.”

Bluespec will participate at DesignCon 2006 in two events:

**February 6, 1:30 p.m.: TechForum Tutorial: TF-MP1: Beyond RTL:
Advanced Digital System Design**

Description: This tutorial will introduce new technologies and advanced techniques to cope with the complexity of designing modern digital chips which, increasingly, are complete systems containing multiple processors, complex IP blocks and high-speed buses and interconnection networks.

Presented by: Rishiyur Nikhil, CTO, of Bluespec

**February 8, 9 a.m.: Management Forum Panel: Make Technical On-Line
Marketing Truly “Technical” — From Tactics to Strategy: What Works and What
Doesn’t?**

Chairperson: Ed Y. Zhang of Hometown Innovation Automation Inc.

Bluespec panelist: George Harper

About Bluespec

Bluespec Inc. manufactures an industry standards-based Electronic Design Automation (EDA) toolset that significantly raises the level of abstraction for hardware design while retaining the ability to automatically synthesize high-quality RTL, without compromising speed, power or area. The toolset, the only one focused on control and complex datapaths, allows ASIC and FPGA designers to reduce design time, bugs and re-spins that contribute to product delays and escalating costs. More information can be found on www.bluespec.com or by calling (781) 250-2200.

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