

## ***NEWS RELEASE***

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### **Bluespec ESL Synthesis Training Available Online** *Video-Based Course Offered Free of Charge at Demos on Demand*

**Waltham, Mass.** — March 28, 2006 — In-depth, multimedia training showcasing Bluespec Inc.'s electronic system level (ESL) synthesis and how it is used to design complex control logic and datapath circuits is available now at:

[http://www.demosondemand.com/dod/proddemos/vendors/pd\\_bluespec.aspx](http://www.demosondemand.com/dod/proddemos/vendors/pd_bluespec.aspx).

Twelve discrete sessions, roughly 60-minutes each, led by Bluespec's Chief Technology Officer Rishiyur S. Nikhil, are offered free of charge to the chip design community and represent the standard training sessions that Bluespec provides customers as a basic foundation for their first designs. They are featured alongside a video-based, one hour technical overview presentation, also led by Dr. Nikhil, of Bluespec's ESL Synthesis toolset.

"The free online program is a terrific addition to the onsite training and personalized support we provide our customers," says George Harper, Bluespec's vice president of marketing. "We've been searching for a way to scale the demands to make ESL synthesis significantly more accessible. The online training is a great answer when customers ask: What are the resources for scaling Bluespec across my organization? How can one of my designers, who missed training, quickly get up to speed?"

The training covers such ESL Synthesis topics as: rules, rule scheduling, module hierarchy and interfaces, multiple clock domains and compiling, running and debugging. Support resources include labs, downloadable PowerPoint training slides, and documentation are included. The sessions are hosted by and were co-developed with Demos on Demand (<http://www.demosondemand.com>).

For more details, contact George Harper, vice president of Bluespec, who can be reached at (781) 250-2200 or via email at [george.harper@bluespec.com](mailto:george.harper@bluespec.com).

### **About Bluespec**

Bluespec Inc. manufactures an industry standards-based Electronic Design Automation (EDA) toolset that significantly raises the level of abstraction for hardware design while retaining the ability to automatically synthesize high-quality RTL, without compromising speed, power or area. The toolset, the only one focused on control and complex datapaths, allows ASIC and FPGA designers to reduce design time, bugs and re-spins that contribute to product delays and escalating costs. More information can be found on [www.bluespec.com](http://www.bluespec.com) or by calling (781) 250-2200.

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